

PATMOS2008

International Workshop on Power and Timing Modeling, Optimization and Simulation

Lisbon, Portugal • September 10-12, 2008

2nd Call for Papers

PATMOS 2008 is the eighteenth in a series of international workshops. The PATMOS meeting has evolved into a leading scientific event, where industry and academia meet to debate power and timing aspects in modern integrated circuit and system design. PATMOS 2008 is organized by INESC-ID and the Technical University of Lisbon, in Lisbon, Portugal. For this edition, we have the privilege to announce two outstanding keynote speakers: Ted Vucurevich, Cadence, and Sani Nassif, IBM.

The PATMOS objective is to provide a forum to discuss and investigate emerging challenges in methodologies and tools for the design of upcoming generations of integrated circuits and systems, including reconfigurable hardware such as FPGAs. The technical program will focus on timing, performance and power consumption as well as architectural aspects with particular emphasis on modeling, design, characterization, analysis and optimization. The emphasis of the workshop is on, but is not limited to, the following topics:

Timing and Performance

- Methodologies and tools for the analysis, design and verification of timing and performance properties of integrated circuits and systems at all levels of abstraction;
- · Statistical Timing Analysis, Design for Yield, Design for Manufacturability;
- Special timing or performance related topics, e.g. crosstalk, synchronization, GALS, side-channel attacks.

Power Dissipation

- Design techniques for low power circuits and systems at all levels of abstraction;
- Methods and tools for analysis, characterization, design and optimization of the power consumption;
- Low power architectures and libraries;
- Special power related topics, e.g. low voltage, leakage power, power grid, interconnect power, clock tree power, power aware test pattern generation, thermal effects.

Design Experience and Case Studies

 Examples, test cases, benchmarks or design studies which present innovative solutions for timing, performance or power consumption related design challenges.

Contributions are invited for regular presentations and discussion sessions. Prospective authors are invited to submit their complete manuscript, no later than March 24, 2008, including a 100-word abstract and illustrations, in A4 camera-ready format, not exceeding 10 pages or 5000 words. Submitted papers will be reviewed formally and anonymously by several reviewers. The PATMOS 2008 proceedings will be published by Springer in the *Lecture Notes in Computer Science* (LNCS) series. Electronic submission is required and should follow the LNCS style. Selected papers will be included in a special issue of *Journal of Embedded Computing*, published by IOS Press. Proposals for panel sessions and special sessions are encouraged and must also be received no later than March 24, 2008. Please refer to the workshop web page for further information.

Deadlines

Paper submission Notification of acceptance Camera-ready version	May 30, 2008	(extended deadline)
Special/Panel session proposal	•	

Lisbon is the capital and largest city of Portugal. Situated in the west coast of the country, where the Tagus river meets the Atlantic Ocean, it is the westernmost capital in mainland Europe. Lisbon is known as the white city, thanks to its unique luminosity. The light, the atmosphere and the climate offer marvelous walks all over the city. Lisbon is a very lively city, with a wealth of cultural events happening every day, and a vibrant nightlife. A huge selection of restaurants is available, featuring Portuguese and international cuisine. In September the weather is pleasant, with sunny days, warm temperatures and little or no rainfall. The city's public transportation system is far-reaching and reliable, and has the Metro (subway) as its main artery. Lisbon can be easily reached by daily direct flights from major international cities.

http://algos.inesc-id.pt/patmos

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